

BCA 2nd Semester
Computer Organization and architecture
Paper Code-BCA-1201

1. What is bus? Draw and explain the bus structure using multiplexer for 4-bit data. What is tristate buffer?
2. Explain operation of SRAM with diagram. Give the difference between SRAM and DRAM.
3. Construct memory architecture for 1KX8 ROM using 512X8 ROM IC. Hence give its memory map. What is memory hierarchy?
4. How an Associative mapping is performed? Explain with diagram. What are the advantage and disadvantage of this mapping?
5. What is hardwired control unit? Explain its structure.
6. What are the differences between Horizontal and Vertical microprogramming?
7. Write the basic Three, Two, One, Zero addressing instruction code for the expression $X=(A+B)*(C+D)$.
8. Give the difference between CISC and RISC

Send answer script to Mail Id- kncontai@gmail.com